

<b>Notice of References Cited</b>		Application/Control No.	Applicant(s)/Patent Under Reexamination CAVANAGH ET AL.	
		Examiner Jason Proctor	Art Unit 2123	Page 1 of 1

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*		Document Number Country Code-Number-Kind Code	Date MM-YYYY	Name	Classification
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	B	US-6,278,963	08-2001	Cohen, Alain	703/17
	C	US-			
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	J	US-			
	K	US-			
	L	US-			
	M	US-			

**FOREIGN PATENT DOCUMENTS**

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**NON-PATENT DOCUMENTS**

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	U	C.R. Pon, R. Saleh, T. Kwasniewski; "Distributed Circuit Simulation using Waveform Relaxation in a Slotted-Ring Architecture"; Sept. 1994; Electrical and Computer Engeineer 1994 Conference Proceedings; pages 545-548 vol. 2
	V	Richard M. Fujimoto, "Parallel and Distributed Discrete Event Simulation: Algorithms and Applications", 1993, Proceedings of the 19993 Winter Simulation Conference, pages 106-114
	W	Jörg Keller, Thomas Rauber, Bernd Rederlechner; "Conservative Circuit Simulation on Shared-Memory Multiprocessors"; 1996; pages 126-134
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\*A copy of this reference is not being furnished with this Office action. (See MPEP § 707.05(a).)  
Dates in MM-YYYY format are publication dates. Classifications may be US or foreign.